

LISTING OF CLAIMS, REFLECTING AMENDMENTS:

The embodiments of the invention in which an exclusive property or privilege is claimed are defined as follows:

1. (Currently Amended) A method of restoring faulty connections in an integrated circuit device having a plurality of memory circuit elements and a plurality of connecting pins connected to said memory circuit elements, the method comprising the steps of:

using an integrated circuit programming device, executing a device read command;

obtaining an error message from said programming device identifying certain of said connecting pins appearing to be disconnected from a memory circuit element; ~~and~~

subsequent in time to obtaining said error message, connecting a voltage supply to said certain of said connecting pins appearing to be disconnected from said memory circuit element; and

applying a voltage signal to said certain of said connecting pins, with said voltage signal being applied for a predetermined period of time; and

restoring faulty connections between said memory circuit element and at least one of said connecting pins appearing to be disconnected from said memory circuit element.

2. (Original) The method in accordance with claim 1 wherein said signal comprises a read command for reading said memory device.

3. (Original) The method in accordance with claim 1 wherein said step of applying comprises applying said signal to said certain connecting pins through a resistor.

4. (Original) The method in accordance with claim 3 wherein said signal has value of at least 5 volts.

5. (Original) The method in accordance with claim 4 wherein said signal has value of negative five volts.

6. (Original) The method in accordance with claim 5 wherein said resistor has a value of 100 kilo-ohms.

7. (Currently Amended) A method of restoring faulty elements internal to an integrated circuit device having at least one external connecting pin indicating a fault connection, said method comprising the ~~step of applying a voltage signal of a predefined level to said external connecting pin.~~steps of:

applying a voltage signal of a predefined level to said external connecting pin; and
restoring a faulty connection between said external connecting pin and elements of
said integrated circuit device as a result of said application of said voltage signal, subject to
the condition that said external connecting pin is not completely disconnected from said
elements of said integrated circuit device.